

## ABSTRACT OF THE DISCLOSURE

In the cache memory system, tag memory RAM modules and cache memory RAM sections each capable of switching a state between an ordinary state and a low consumption power state are connected in parallel. An n-way set associative constitution in which all the tag memory RAM modules and all the cache memory RAM modules are activated in the ordinary state, is switched to/from a 1-way direct map constitution in which only a pair of the tag memory RAM module and the cache memory RAM module are activated in the ordinary state and the remaining tag memory RAM modules and cache memory RAM modules are turned into the low consumption power state based on a value of a request address, according to a power mode signal.